

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FIRST NAMED APPLICANT : Junichi KISHIDA Confirmation No.: 5285
TITLE OF THE INVENTION : BOOTABLE NAND FLASH MEMORY
 ARCHITECTURE
SERIAL NO. : 10/716,159
FILED : November 18, 2003
ART UNIT : 2188
CUSTOMER NO. : 23380

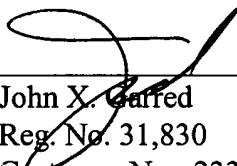
STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sirs:

1. More than 18 months have passed since the filing of this application on November 18, 2003. The last action taken on this application was the Notice of Publication which was received June 15, 2005. No further communication has been received from the Patent and Trademark Office indicating action on this application.
2. Kindly advise the undersigned of the present status of this application.

Respectfully submitted,



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Date: July 8, 2005

MAIL CERTIFICATION UNDER 37 C.F.R. §1.8

I hereby certify that, on the date shown above, this correspondence (along with any other paper referred to as being attached or enclosed) is being deposited with the United States Postal Service in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" with sufficient postage as first class mail.

Mary Erne, Jr.
(Typed or Printed Name of Person Mailing Paper)


(Signature of Person Mailing Paper)